

Refine Search

Search Results -

Term	Documents
CACHE	28809
CACHES	8046
(1 NEAR8 CACHE).USPT.	27
(L1 NEAR8 CACHE).USPT.	27

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
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 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

-

-

Search History

DATE: Thursday, April 22, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u>
side by side			result set
DB=USPT; PLUR=YES; OP=OR			
L2	L1 near8 cache	27	L2
L1	(size\$1 or width\$1 or length\$1) near5 instruction\$1 near6 (sel or select\$6 or mux or multiplex\$3)	635	L1

END OF SEARCH HISTORY

L6 (712/2-300)[CCLS] 9686 L6

L5 (712/2-300)![CCLS] 9686 L5

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES;
OP=OR

L4 L2 and (select\$3 or mux or multiplex\$3 or sel) near9
compar\$7 40 L4

L3 L2 and (select\$3 or mux or multiplex\$3 or sel) 92 L3

L2 L1 near7 cache 106 L2

(width\$1 or length\$1 or size\$1) near6 (instruction\$1
or opcode\$1) near6 (map\$5 or table\$1 or list\$3 or
associat\$3 or correspond\$3 or respective) 1349 L1

END OF SEARCH HISTORY

Hit List

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[Generate OACS](#)

Search Results - Record(s) 1 through 20 of 27 returned.

1. Document ID: US 6591414 B2

L2: Entry 1 of 27

File: USPT

Jul 8, 2003

US-PAT-NO: 6591414

DOCUMENT-IDENTIFIER: US 6591414 B2

TITLE: Binary program conversion apparatus, binary program conversion method and program recording medium

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn De](#)

2. Document ID: US 6526479 B2

L2: Entry 2 of 27

File: USPT

Feb 25, 2003

US-PAT-NO: 6526479

DOCUMENT-IDENTIFIER: US 6526479 B2

TITLE: Method of caching web resources

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn De](#)

3. Document ID: US 6460116 B1

L2: Entry 3 of 27

File: USPT

Oct 1, 2002

US-PAT-NO: 6460116

DOCUMENT-IDENTIFIER: US 6460116 B1

TITLE: Using separate caches for variable and generated fixed-length instructions

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn De](#)

4. Document ID: US 6412043 B1

L2: Entry 4 of 27

File: USPT

Jun 25, 2002

US-PAT-NO: 6412043

DOCUMENT-IDENTIFIER: US 6412043 B1

h e b b g e e e f

e ef b e

TITLE: Microprocessor having improved memory management unit and cache memory

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Searches](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw](#)

5. Document ID: US 6367001 B1

L2: Entry 5 of 27

File: USPT

Apr 2, 2002

US-PAT-NO: 6367001

DOCUMENT-IDENTIFIER: US 6367001 B1

**** See image for Certificate of Correction ****

TITLE: Processor including efficient fetch mechanism for L0 and L1 caches

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Searches](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw](#)

6. Document ID: US 6275927 B1

L2: Entry 6 of 27

File: USPT

Aug 14, 2001

US-PAT-NO: 6275927

DOCUMENT-IDENTIFIER: US 6275927 B1

TITLE: Compressing variable-length instruction prefix bytes

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Searches](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw](#)

7. Document ID: US 6256727 B1

L2: Entry 7 of 27

File: USPT

Jul 3, 2001

US-PAT-NO: 6256727

DOCUMENT-IDENTIFIER: US 6256727 B1

**** See image for Certificate of Correction ****

TITLE: Method and system for fetching noncontiguous instructions in a single clock cycle

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Searches](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw](#)

8. Document ID: US 6253309 B1

L2: Entry 8 of 27

File: USPT

Jun 26, 2001

US-PAT-NO: 6253309

DOCUMENT-IDENTIFIER: US 6253309 B1

TITLE: Forcing regularity into a CISC instruction set by padding instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Specifications	Attachments	Claims	KOMC	Drawn De
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9. Document ID: US 6247123 B1

L2: Entry 9 of 27

File: USPT

Jun 12, 2001

US-PAT-NO: 6247123

DOCUMENT-IDENTIFIER: US 6247123 B1

TITLE: Branch prediction mechanism employing branch selectors to select a branch prediction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Specifications	Attachments	Claims	KOMC	Drawn De
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10. Document ID: US 6209058 B1

L2: Entry 10 of 27

File: USPT

Mar 27, 2001

US-PAT-NO: 6209058

DOCUMENT-IDENTIFIER: US 6209058 B1

**** See image for Certificate of Correction ****

TITLE: Cache management for data transfer control from target disk areas

Full	Title	Citation	Front	Review	Classification	Date	Reference	Specifications	Attachments	Claims	KOMC	Drawn De
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11. Document ID: US 6192465 B1

L2: Entry 11 of 27

File: USPT

Feb 20, 2001

US-PAT-NO: 6192465

DOCUMENT-IDENTIFIER: US 6192465 B1

TITLE: Using multiple decoders and a reorder queue to decode instructions out of order

Full	Title	Citation	Front	Review	Classification	Date	Reference	Specifications	Attachments	Claims	KOMC	Drawn De
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12. Document ID: US 6175908 B1

L2: Entry 12 of 27

File: USPT

Jan 16, 2001

US-PAT-NO: 6175908

DOCUMENT-IDENTIFIER: US 6175908 B1

**** See image for Certificate of Correction ****

TITLE: Variable byte-length instructions using state of function bit of second byte of plurality of instructions bytes as indicative of whether first byte is a prefix byte

h e b b g e e e f

e ef b e

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn De
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13. Document ID: US 6141745 A

L2: Entry 13 of 27

File: USPT

Oct 31, 2000

US-PAT-NO: 6141745

DOCUMENT-IDENTIFIER: US 6141745 A

TITLE: Functional bit identifying a prefix byte via a particular state regardless of type of instruction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn De
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14. Document ID: US 6141376 A

L2: Entry 14 of 27

File: USPT

Oct 31, 2000

US-PAT-NO: 6141376

DOCUMENT-IDENTIFIER: US 6141376 A

TITLE: Single chip communication device that implements multiple simultaneous communication channels

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn De
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15. Document ID: US 6125441 A

L2: Entry 15 of 27

File: USPT

Sep 26, 2000

US-PAT-NO: 6125441

DOCUMENT-IDENTIFIER: US 6125441 A

TITLE: Predicting a sequence of variable instruction lengths from previously identified length pattern indexed by an instruction fetch address

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn De
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16. Document ID: US 6049863 A

L2: Entry 16 of 27

File: USPT

Apr 11, 2000

US-PAT-NO: 6049863

DOCUMENT-IDENTIFIER: US 6049863 A

TITLE: Predecoding technique for indicating locations of opcode bytes in variable byte-length instructions within a superscalar microprocessor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn De
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17. Document ID: US 6041405 A

L2: Entry 17 of 27

File: USPT

Mar 21, 2000

US-PAT-NO: 6041405

DOCUMENT-IDENTIFIER: US 6041405 A

TITLE: Instruction length prediction using an instruction length pattern detector

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KOMC	Drawn De
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 18. Document ID: US 6003128 A

L2: Entry 18 of 27

File: USPT

Dec 14, 1999

US-PAT-NO: 6003128

DOCUMENT-IDENTIFIER: US 6003128 A

TITLE: Number of pipeline stages and loop length related counter differential based end-loop prediction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KOMC	Drawn De
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 19. Document ID: US 5995749 A

L2: Entry 19 of 27

File: USPT

Nov 30, 1999

US-PAT-NO: 5995749

DOCUMENT-IDENTIFIER: US 5995749 A

TITLE: Branch prediction mechanism employing branch selectors to select a branch prediction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KOMC	Drawn De
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 20. Document ID: US 5961638 A

L2: Entry 20 of 27

File: USPT

Oct 5, 1999

US-PAT-NO: 5961638

DOCUMENT-IDENTIFIER: US 5961638 A

TITLE: Branch prediction mechanism employing branch selectors to select a branch prediction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KOMC	Drawn De
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Term	Documents
CACHE	28809
CACHES	8046
(1 NEAR8 CACHE).USPT.	27
(L1 NEAR8 CACHE).USPT.	27

Display Format:

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Refine Search

Search Results -

Term	Documents
(2 AND 7).PGPB,USPT.	35
(L7 AND L2).PGPB,USPT.	35

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Set
Name Query
 side by
 side

Hit Set
Count Name
 result
 set

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L11</u>	l7 and l2	35	<u>L11</u>
<u>L10</u>	l7 and l1	188	<u>L10</u>
<u>L9</u>	L8 and l4	31	<u>L9</u>
<u>L8</u>	l6 or L7	24634	<u>L8</u>
<u>L7</u>	(711/117-221)![CCLS]	16214	<u>L7</u>

L6 (712/2-300)[CCLS] 9686 L6
L5 (712/2-300)![CCLS] 9686 L5

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES;
OP=OR

<u>L4</u>	L2 and (select\$3 or mux or multiplex\$3 or sel) near9 compar\$7	40	<u>L4</u>
<u>L3</u>	L2 and (select\$3 or mux or multiplex\$3 or sel)	92	<u>L3</u>
<u>L2</u>	L1 near7 cache	106	<u>L2</u>
<u>L1</u>	(width\$1 or length\$1 or size\$1) near6 (instruction\$1 or opcode\$1) near6 (map\$5 or table\$1 or list\$3 or associat\$3 or correspond\$3 or respective)	1349	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Term	Documents
CACHE	28809
CACHES	8046
OPCODE\$3	0
OPCODE	4168
OPCODED	6
OPCODEGRP	1
OPCODEJ	1
OPCODEJO	1
OPCODEL	3
OPCODEN	14
OPCODEREG	5
(L1 NEAR10 (OPCODE\$3 OR INSTRUCTION\$1) NEAR12 CACHE).USPT.	35

[There are more results than shown above. Click here to view the entire set.](#)

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Search History

DATE: Thursday, April 22, 2004 [Printable Copy](#) [Create Case](#)

Set
Name Query
 side by
 side

Hit
Count Set
Name
 result set

DB=USPT; PLUR=YES; OP=OR

<u>L5</u>	L1 near10 (opcode\$3 or instruction\$1) near12 cache	35	<u>L5</u>
<u>L4</u>	L1 near10 (opcode\$3 or instruction\$1) near12 cache near4 controller	0	<u>L4</u>
<u>L3</u>	L1 near10 (opcode\$3 or instruction\$1) and cache near4 controller	81	<u>L3</u>
<u>L2</u>	L1 near10 (opcode\$3 or instruction\$1)	859	<u>L2</u>
<u>L1</u>	(size\$1 or length\$1 or width\$1) near6 (mux or multiplex\$5 or select\$3 or sel)	170393	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Term	Documents
CACHE	28809
CACHES	8046
OPCODE\$3	0
OPCODE	4168
OPCODED	6
OPCODEGRP	1
OPCODEJ	1
OPCODEJO	1
OPCODEL	3
OPCODEN	14
OPCODEREG	5
(L1 NEAR10 (OPCODE\$3 OR INSTRUCTION\$1) NEAR12 CACHE).USPT.	35

[There are more results than shown above. Click here to view the entire set.](#)

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DATE: Thursday, April 22, 2004 [Printable Copy](#) [Create Case](#)

Set
Name Query
side by side

Hit
Count Set
Name
result set

• *DB=USPT; PLUR=YES; OP=OR*

<u>L5</u>	L1 near10 (opcode\$3 or instruction\$1) near12 cache	35	<u>L5</u>
<u>L4</u>	L1 near10 (opcode\$3 or instruction\$1) near12 cache near4 controller	0	<u>L4</u>
<u>L3</u>	L1 near10 (opcode\$3 or instruction\$1) and cache near4 controller	81	<u>L3</u>
<u>L2</u>	L1 near10 (opcode\$3 or instruction\$1)	859	<u>L2</u>
<u>L1</u>	(size\$1 or length\$1 or width\$1) near6 (mux or multiplex\$5 or select\$3 or sel)	170393	<u>L1</u>

END OF SEARCH HISTORY